

EPISIL

Building Bridges to a Greener Future



Enhanced System
Efficiency



High-temp
Resistance



High Voltage
Tolerance

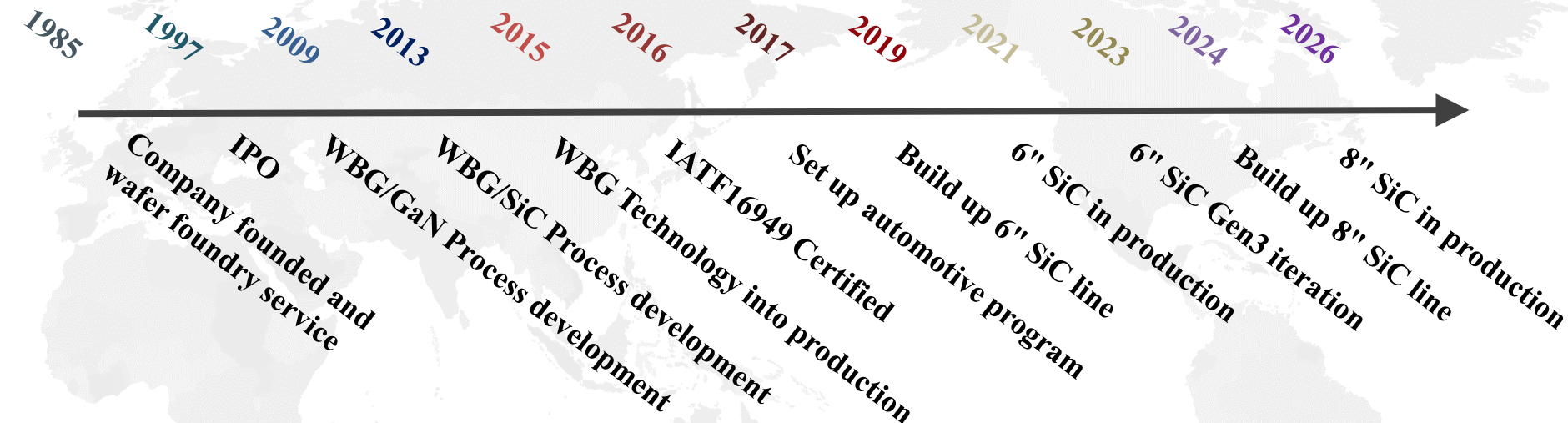
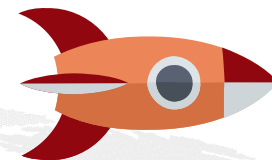


High Current
Tolerance

漢磊科技股份有限公司

EPISIL Technologies Inc.

Milestone



永續發展藍圖

掌握ESG時代永續契機

基礎建置

強化基礎建置

了解ESG法規及趨勢
盤點ESG績效
製作永續報告書
(含現、包含GRI、SASB、TCFD)

管理精進

建立願景策略

評估ESG趨勢對業務之影響
擬定ESG永續策略
建立ESG行動方案
設定ESG精進目標與指標
凝聚ESG內部共識

戰略轉型

發展ESG品牌與商機

發展ESG趨勢業務機會
持續精進ESG行動
提升ESG評比
建立ESG品牌形象

逐步達成集團
ESG標竿與目標

Technology Service (GaN / SiC)

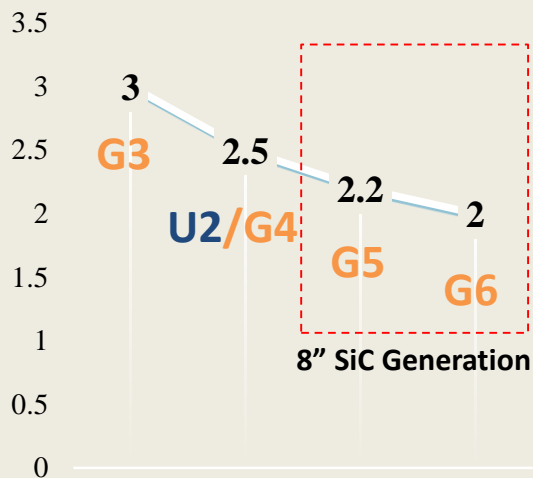
[GaN] We offer 6" GaN/Si epi and E-mode HEMT process in one step shopping

HEMT Process List

- E-Mode GaN FET : 0.5um /30V ~350V
- E-Mode GaN IC : 0.5um ~100V
- The E-mode process platform is exclusive process for specific customer only.

[SiC] Product portfolio from 650V to 3300V of MOS (8mΩ ~ 60Ω)

Ron.sp(mOhm-cm²)



[AEC-Q101grade]

6" Planar Technology



Ron_sp ≤ 3.0mΩ.cm²

Cell pitch = 4.6um

thin wafer(160um)*

2024(Q3)

[Mass production]



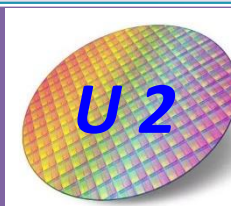
Ron_sp ≤ 2.5mΩ.cm²

Cell pitch = 3.8um

thin wafer(100um)*

2025(Sep)

6" Trench Technology



Ron_sp ≤ 2.5mΩ.cm²

Cell pitch < 2.8um

thin wafer(100um)*

2025(Q4)

[Co-develop, one customer]

Porting 6" process both of G3 & G4 planar devices into 8" SiC Process in 2026

Quality & ESH Certifications

Quality Assurance

2022	Notification of Sony GP Certification
2021	ISO 9001 : 2015 Certified
	IATF 16949 : 2016 Certified
	Granted VDA6.3 Grade A from Seven Major Accounts

Environmental and Occupational Health & Safety Management System

2021	ISO 45001 : 2018 Certified
	ISO 14001 : 2015 Certified
	ISO 14064 : 2006 Certified

